

Apache's RedHawk Power Integrity Solution Adopted by AppliedMicro for their SoC and Mixed-Signal Designs

SAN JOSE, Calif. – April 28, 2009 – Apache Design Solutions, the technology leader in power and noise integrity for chip, package, and system convergence, today announced that AppliedMicro (NASDAQ: AMCC), a global leader in energy conscious computing and communications solutions for Datacenter, Telecom, Enterprise and Consumer Applications, has adopted Apache's RedHawk, a full-chip dynamic power integrity solution, for accurate power analysis, sign-off, and optimization of their system-on-chip (SoC) and mixed-signal designs. AppliedMicro selected RedHawk based on its product maturity and the capacity to handle designs used in their multi-core products for global wide area network (WAN) and storage area network (SAN) applications. AppliedMicro's adoption is also based on Apache's roadmap and track record for SoC power and noise integrity.

“In the competitive consumer market, balancing the cost and design risk allows us to gain market share,” said Michael Raam, vice president of engineering at AppliedMicro. “RedHawk offers us the accuracy and capacity needed to analyze our complex SoC designs from early prototyping to signoff. In addition, Apache's broad product offering in power and noise integrity from SoC to mixed-signal IPs and high-speed I/Os make them a good technology partner as we aggressively move into post sub-micron designs.”

“We are very pleased to have AppliedMicro place their confidence in our advanced low-power solutions,” said Andrew Yang, CEO at Apache. “With over 100 customers worldwide, the growing adoption of our solutions demonstrates Apache's continuing execution in delivering the best-in-class power and noise integrity products.”

About RedHawk

RedHawk is a full-chip Vectorless Dynamic™ power sign-off solution for high-performance SoC designs, including those utilizing advanced low power design techniques. Certified by TSMC and Common Platform Reference Flows and correlated

with silicon measurements and SPICE, RedHawk delivers the industry's first mesh pattern recognition technology (MPR) and hierarchical dynamic power engine to handle designs in the excess of 500 million gates with sign-off accuracy. RedHawk enables designers to identify dynamic "hot spots," examine its impact on timing, accurately pinpoint the cause of dynamic voltage drop, and automatically repair the source of supply noise. It considers dynamic power issues such as simultaneous switching output (SSO) for core, memory, clock, and I/O, as well as the impact of on-chip inductance, decoupling capacitance, and package / system parasitics.

About Apache Design Solutions

Apache delivers the industry's leading global power and noise analyses platform solutions for Chip-Package-System convergence. Apache's innovative platforms address the unique power and noise challenges associated with specific design domains such as SoC (digital), analog and custom IP, and System (IC package, SiP, PCB), while providing a co-analysis environment that integrates the SoC and System worlds. From early-stage to sign-off, Apache's products are adopted by over 100 customers including 90% of the top IDM, fabless semiconductor, and foundries for cost reduction, risk mitigation, and time-to-market improvements. Apache is a global company with over 150 employees and R&D centers and direct sales / support offices worldwide. For more information, visit www.apache-da.com.

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